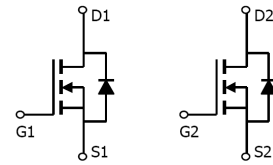


Feature

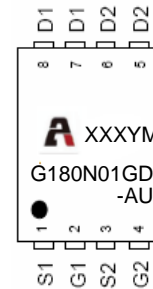
- 100V,25A
 $R_{DS(ON)} < 18m\Omega @ V_{GS}=10V$ TYP:15 m Ω
 $R_{DS(ON)} < 24m\Omega @ V_{GS}=4.5V$ TYP:20 m Ω
- Split Gate Trench Technology
- Lead free product is acquired
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- $T_{jmax}=175^{\circ}C$
- AEC-Q101 qualified

Application

- PWM applications
- Load Switch
- Power management



Schematic Diagram



Marking and pin assignment

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
G180N01GD-AU	APG180N01GD-AU	PDFN5X6-D	13 inch	-	5000

ABSOLUTE MAXIMUM RATINGS ($T_J=25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_c=25^{\circ}C$)	I_D	25	A
Continuous Drain Current ($T_c=100^{\circ}C$)	I_D	17	A
Pulsed Drain Current ⁽¹⁾	I_{DM}	75	A
Single Pulsed Avalanche Energy ⁽²⁾	E_{AS}	28	mJ
Power Dissipation	P_D	103	W
Thermal Resistance from Junction to Case ⁽⁴⁾	$R_{\theta JC}$	1.45	$^{\circ}C/W$
Thermal Resistance- Junction to Ambient	$R_{\theta JA}$	65	$^{\circ}C/W$
Junction Temperature	T_J	175	$^{\circ}C$
Storage Temperature	T_{STG}	-55~ +175	$^{\circ}C$

MOSFET ELECTRICAL CHARACTERISTICS(T_J=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D =250μA	100	-	-	V
Zero gate voltage drain current	I _{DSS}	V _{DS} =100V, V _{GS} = 0V	-	-	1	μA
Gate-body leakage current	I _{GSS}	V _{GS} =±20V, V _{DS} = 0V	-	-	±100	nA
Gate threshold voltage ⁽³⁾	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1	1.8	2.5	V
Drain-source on-resistance ⁽³⁾	R _{DS(on)}	V _{GS} =10V, I _D =20A	-	15	18	mΩ
		V _{GS} =4.5V, I _D =15A	-	20	24	
Dynamic characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f =1MHz	-	1130	-	pF
Output Capacitance	C _{oss}		-	496	-	
Reverse Transfer Capacitance	C _{rss}		-	60	-	
Switching characteristics						
Turn-on delay time	t _{d(on)}	V _{DD} =50V, I _D =20A, R _L =6Ω V _{GS} =10V, R _G =3Ω	-	46	-	ns
Turn-on rise time	t _r		-	55	-	
Turn-off delay time	t _{d(off)}		-	249	-	
Turn-off fall time	t _f		-	105	-	
Total Gate Charge	Q _g	V _{DS} =50V, I _D =20A, V _{GS} =10V	-	30	-	nC
Gate-Source Charge	Q _{gs}		-	6	-	
Gate-Drain Charge	Q _{gd}		-	8.2	-	
Source-Drain Diode characteristics						
Diode Forward voltage ⁽³⁾	V _{DS}	V _{GS} =0V, I _S =10A	-	-	1.2	V
Diode Forward current ⁽⁴⁾	I _S		-	-	25	A

Notes:

1. Repetitive Rating: pulse width limited by maximum junction temperature
2. EAS Condition: T_J=25°C, V_{DD}=20V, R_G=25 Ω, L=0.5mH
3. Pulse Test: pulse width≤300μs, duty cycle≤2%
4. Surface Mounted on FR4 Board, t≤10 sec

Typical Performance Characteristics

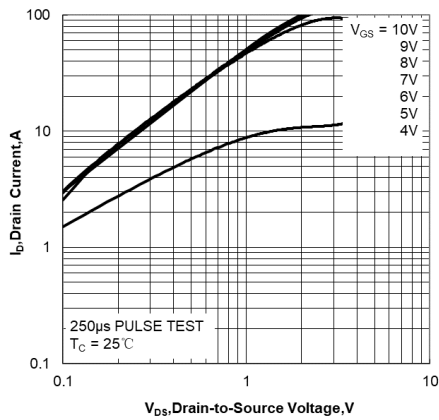


Figure 1. Output Characteristics

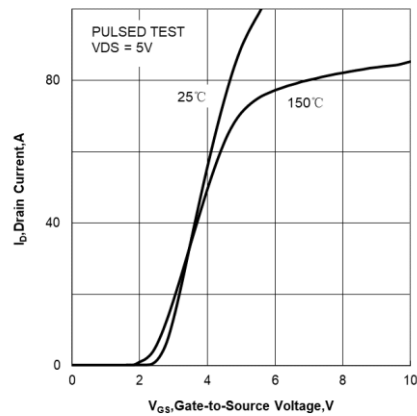


Figure 2. Transfer Characteristics

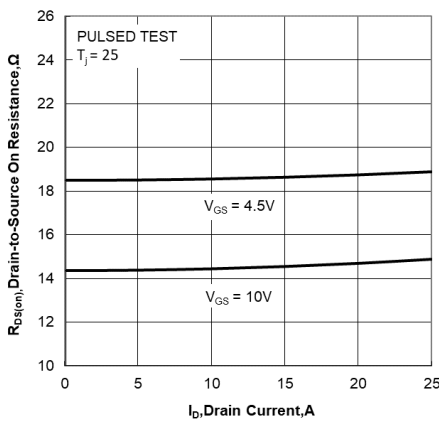


Figure 3. Drain-to-Source On Resistance vs Drain Current

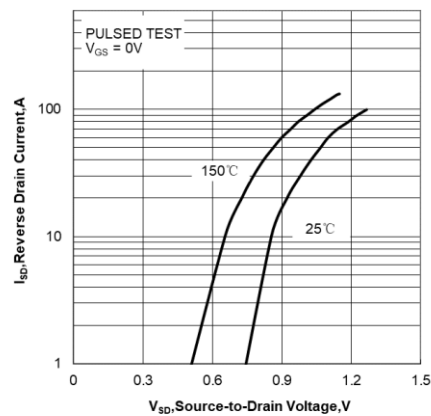


Figure 4. Body Diode Forward Voltage vs Source Current and Temperature

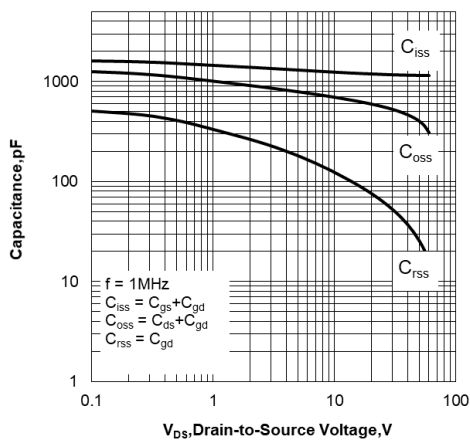


Figure 5. Capacitance Characteristics

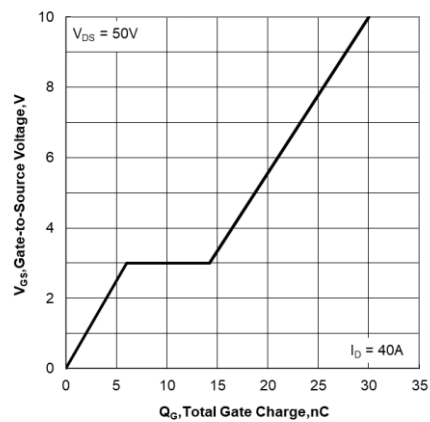


Figure 6. Gate Charge Characteristics

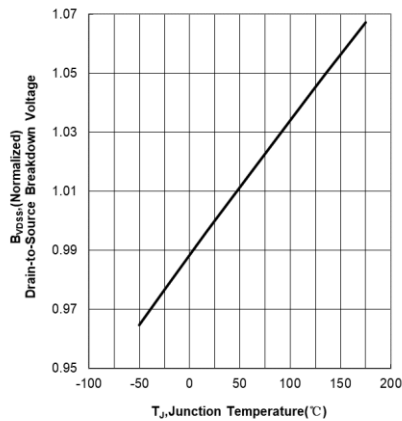


Figure 7. Normalized Breakdown Voltage vs Junction Temperature

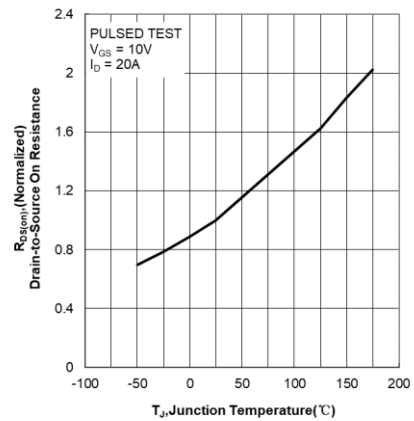


Figure 8. Normalized On Resistance vs Junction Temperature

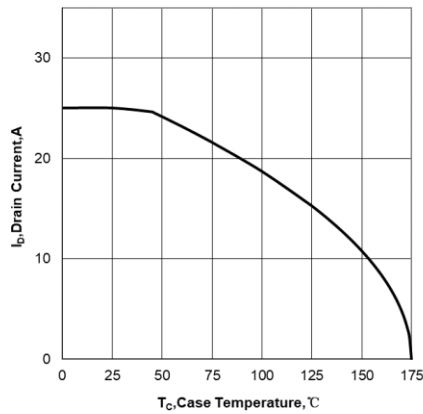


Figure 9. Maximum Continuous Drain Current vs Case Temperature

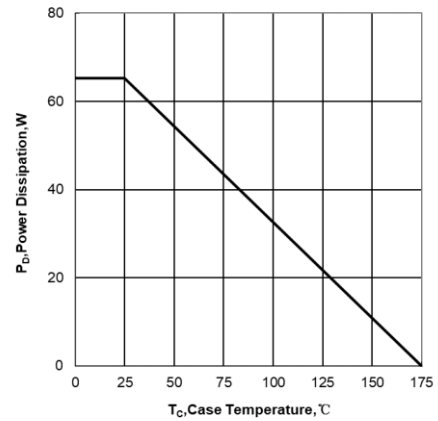


Figure 10. Maximum Power Dissipation vs Case Temperature

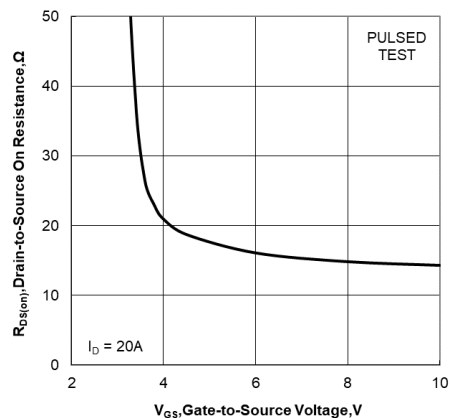


Figure 11. Drain-to-Source On Resistance vs Gate Voltage and Drain Current

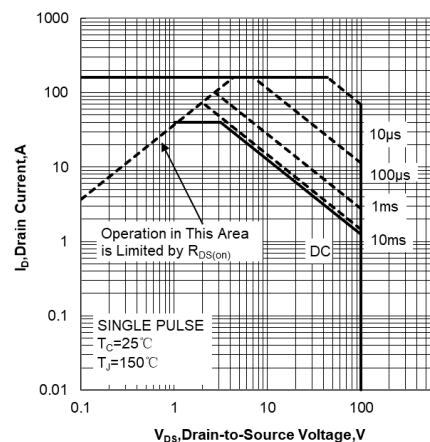


Figure 12. Maximum Safe Operating Area

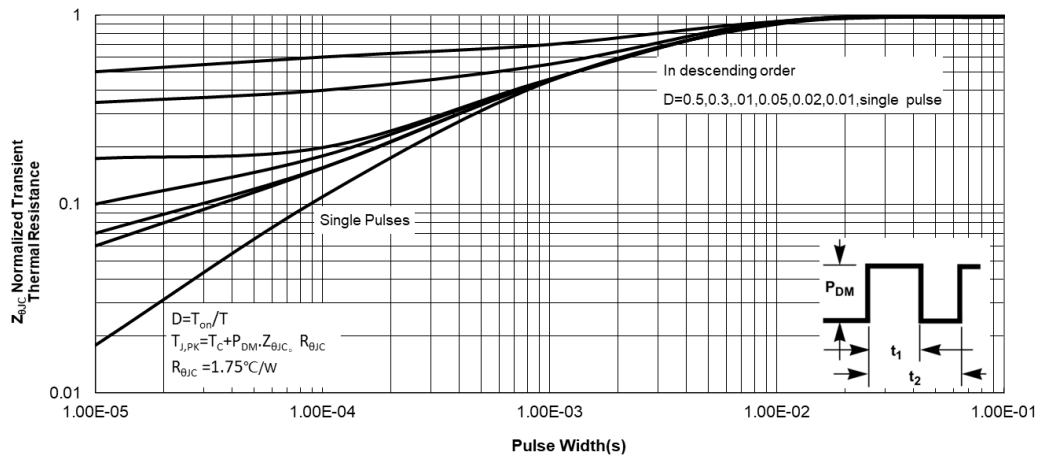
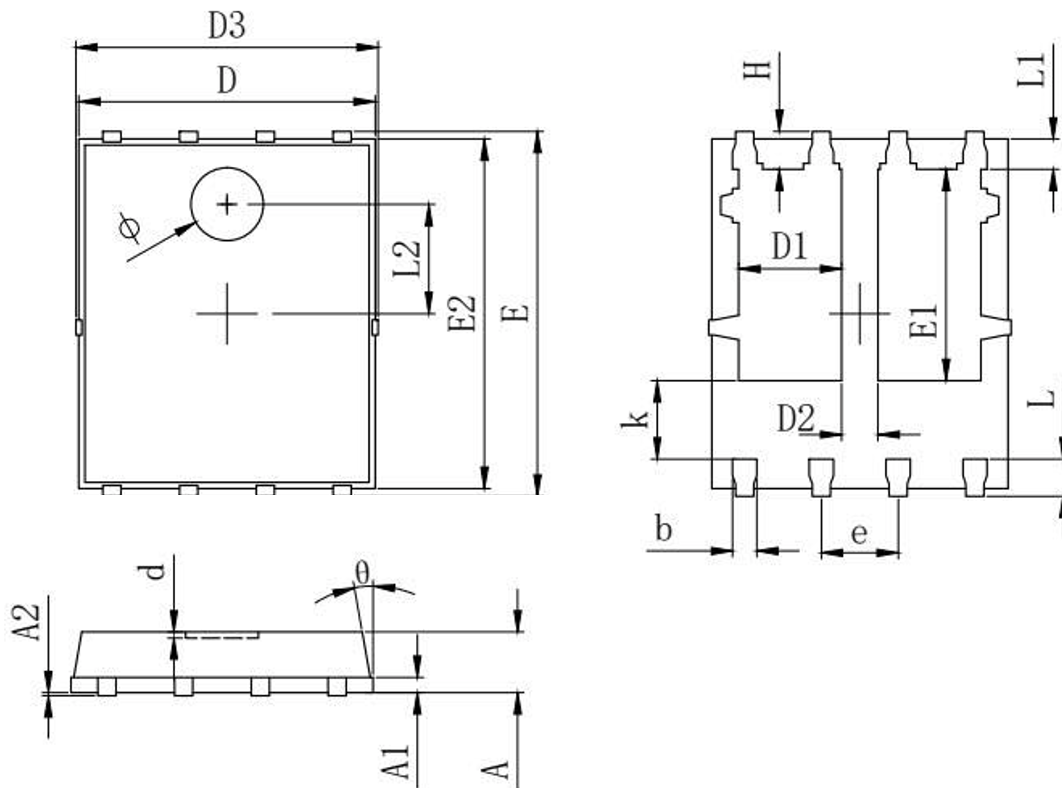


Figure 13. Maximum Effective Transient Thermal Impedance, Junction-to-Case

PDFN5X6 Package Information



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	0.900	1.000	1.100
A1	0.254 REF.		
A2	0 [~] 0.05		
D	4.824	4.900	4.976
D1	1.605	1.705	1.805
D2	0.500	0.600	0.700
D3	4.924	5.000	5.076
E	5.924	6.000	6.076
E1	3.375	3.475	3.575
E2	5.674	5.750	5.826
b	0.350	0.400	0.450
e	1.270 TYP.		
L	0.534	0.610	0.686
L1	0.424	0.500	0.576
L2	1.800 REF.		
k	1.190	1.290	1.390
H	0.549	0.625	0.701
θ	8°	10°	12°
ϕ	1.100	1.200	1.300
d			0.100

Revision History

Revision	Release	Remark
V1.0	2023/01/23	Initial Release

Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Allpower assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior notice.