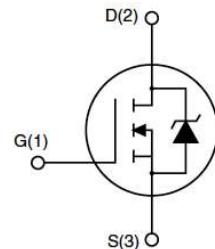


# AP150N03G

## N-Channel Power MOSFET

### Features

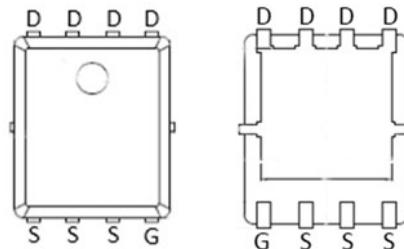
- 30V,150A
- $R_{DS(ON)} = 2.4 \text{ m}\Omega$  (Typ.) @  $V_{GS} = 10\text{V}$
- $R_{DS(ON)} = 4.5 \text{ m}\Omega$  (Typ.) @  $V_{GS} = 4.5\text{V}$
- Low Total Gate Charge
- Low Reverse Transfer Capacitance
- Improved dv/dt Capability
- Fast Switching Speed



### Application

- Load Switch
- PWM Application

### Package



PDFN5X6-8L

### Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Max.	Units
$V_{DSS}$	Drain-Source Voltage		30	V
$V_{GSS}$	Gate-Source Voltage		$\pm 20$	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ\text{C}$	150	A
		$T_C = 100^\circ\text{C}$	105	A
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>		570	A
$P_D$	Power Dissipation	$T_C = 25^\circ\text{C}$	154	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		0.97	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		60	$^\circ\text{C}/\text{W}$
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +175	$^\circ\text{C}$

# AP150N03G

## N-Channel Power MOSFET

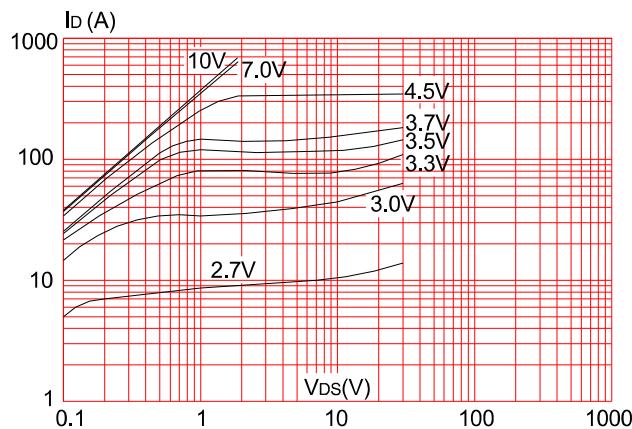
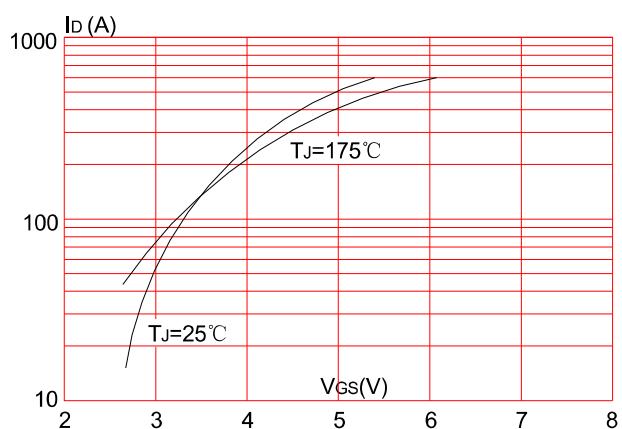
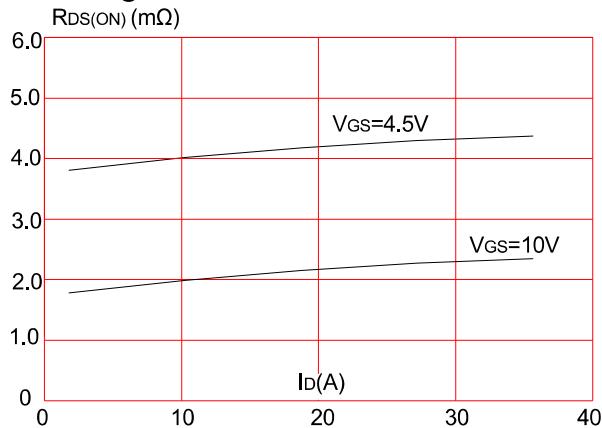
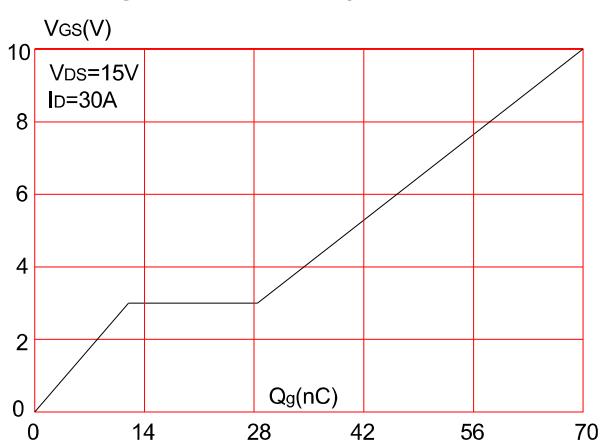
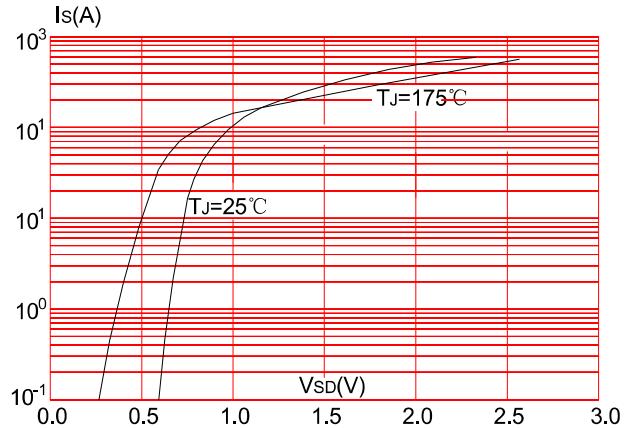
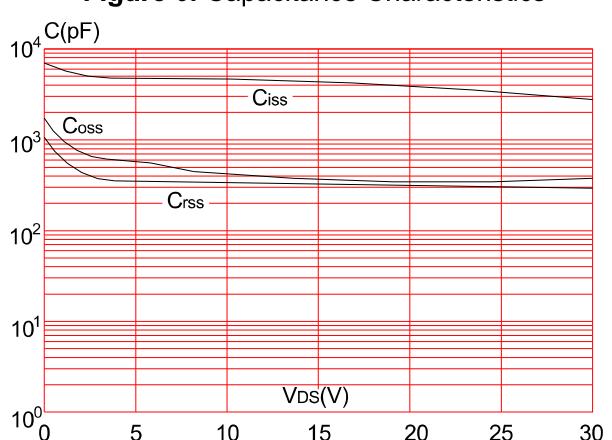
### Electrical Characteristics ( $T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	30	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$	--	--	1	$\mu\text{A}$
		$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}, T_J = 55^\circ\text{C}$	--	--	5	
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{GS} = \pm 20\text{V}$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.7	2.4	V
Drain-Source On-Resistance (Note3)	$R_{DS(\text{on})}$	$V_{GS} = 10\text{V}, I_D = 30\text{A}$	--	2.4	3.2	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 20\text{A}$	--	4.5	6.2	$\text{m}\Omega$
Forward Transconductance (Note3)	$g_{fs}$	$V_{DS} = 10\text{V}, I_D = 20\text{A}$	20.8	--	--	S
<b>Dynamic</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{V}, V_{DS} = 25\text{V}, f = 1.0\text{MHz}$	--	3400	--	pF
Output Capacitance	$C_{oss}$		--	356	--	
Reverse Transfer Capacitance	$C_{rss}$		--	308	--	
Total Gate Charge	$Q_g$	$V_{DD} = 15\text{V}, I_D = 30\text{A}, V_{GS} = 10\text{V}$	--	70	--	nC
Gate-Source Charge	$Q_{gs}$		--	12	--	
Gate-Drain Charge	$Q_{gd}$		--	16	--	
Turn-on Delay Time	$t_{d(\text{on})}$	$V_{DD} = 15\text{V}, I_D = 60\text{A}, R_G = 2\Omega$	--	11	--	ns
Turn-on Rise Time	$t_r$		--	120	--	
Turn-off Delay Time	$t_{d(\text{off})}$		--	25	--	
Turn-off Fall Time	$t_f$		--	60	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_s$	$T_C = 25^\circ\text{C}$	--	--	130	A
Pulsed Diode Forward Current	$I_{\text{SM}}$		--	--	520	
Body Diode Voltage	$V_{SD}$	$T_J = 25^\circ\text{C}, I_{SD} = 30\text{A}, V_{GS} = 0\text{V}$	--	--	1.2	V
Reverse Recovery Time	$t_{rr}$	$I_F = 60\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$	--	56	--	ns
Reverse Recovery Charge	$Q_{rr}$		--	110	--	nC

### Notes

- Repetitive Rating: Pulse Width limited by maximum junction temperature
- $V_{DD} = 30\text{V}, R_G = 25\Omega, L = 0.3\text{mH}, \text{Starting } T_J = 25^\circ\text{C}$
- Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 1\%$

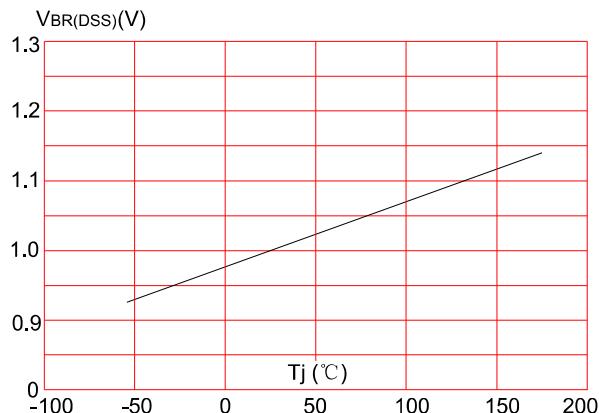
## Typical Performance Characteristics

**Figure 1:** Output Characteristics**Figure 2:** Typical Transfer Characteristics**Figure 3:** On-resistance vs. Drain Current**Figure 5:** Gate Charge Characteristics**Figure 4:** Body Diode Characteristics**Figure 6:** Capacitance Characteristics

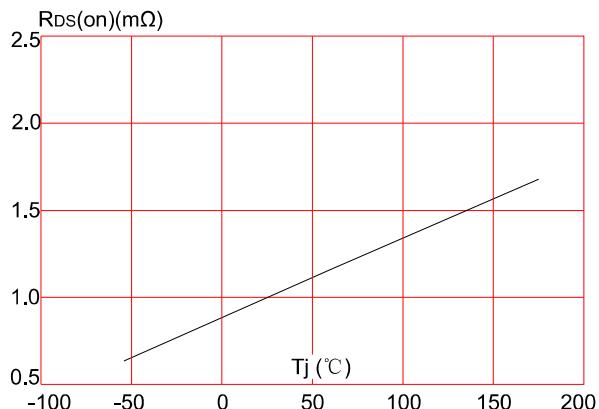
# AP150N03G

## N-Channel Power MOSFET

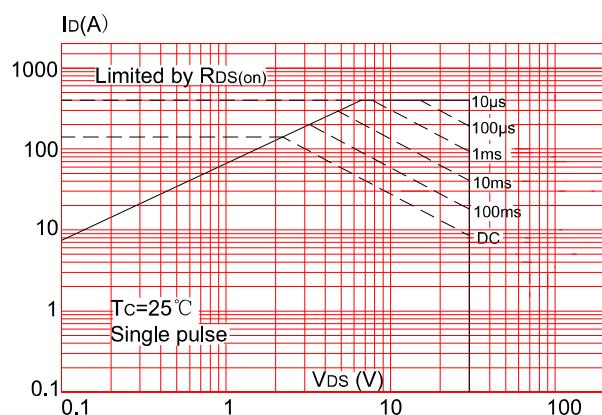
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



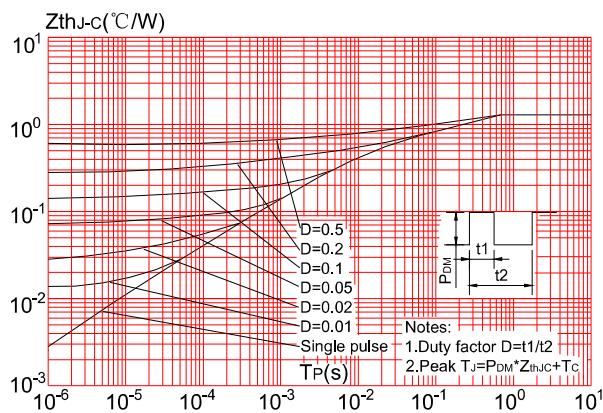
**Figure 8:** Normalized on Resistance vs. Junction Temperature



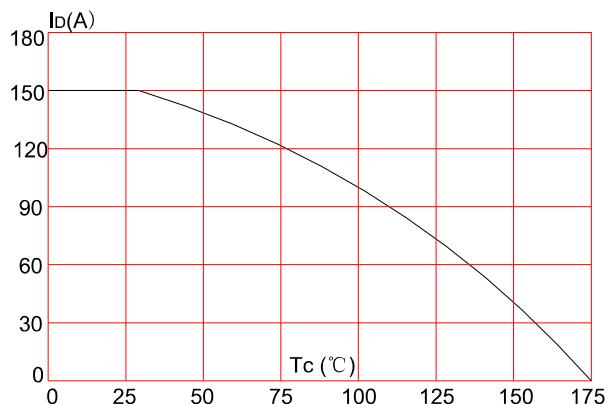
**Figure 9:** Maximum Safe Operating Area



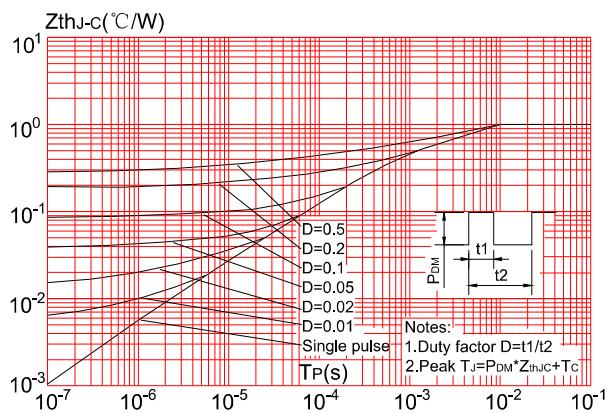
**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case (TO-252)



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



**Figure.12:** Maximum Effective Transient Thermal Impedance, Junction-to-Case (TO-263)



# AP150N03G

## N-Channel Power MOSFET

Figure 1 : Gate Charge Test Circuit and Waveform

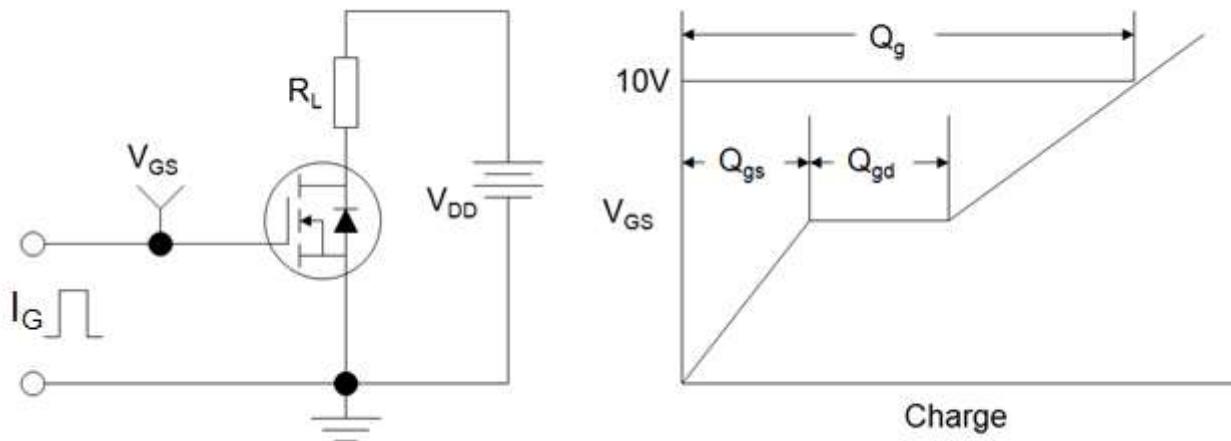


Figure 2 : Resistive Switching Test Circuit and Waveform

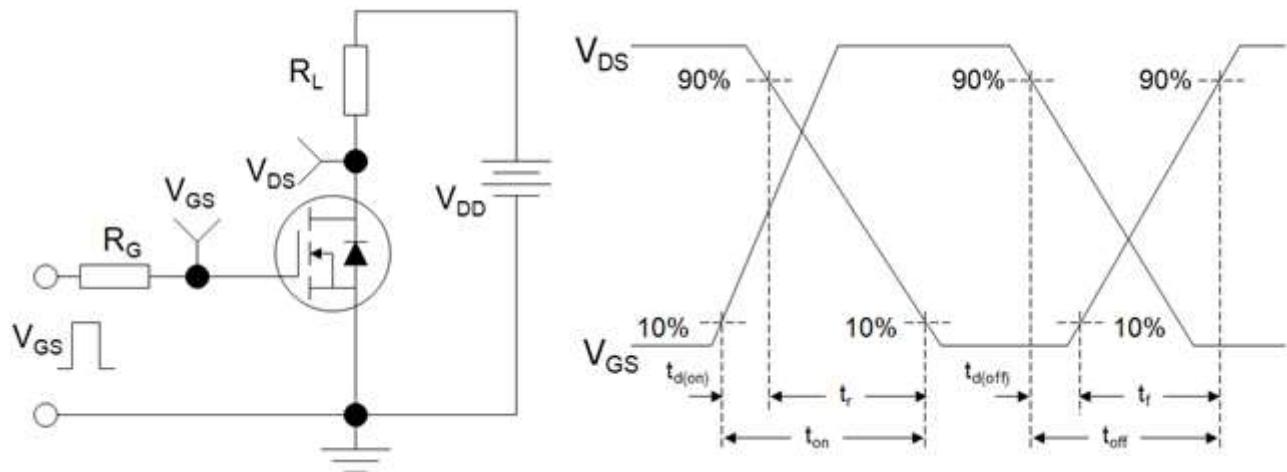
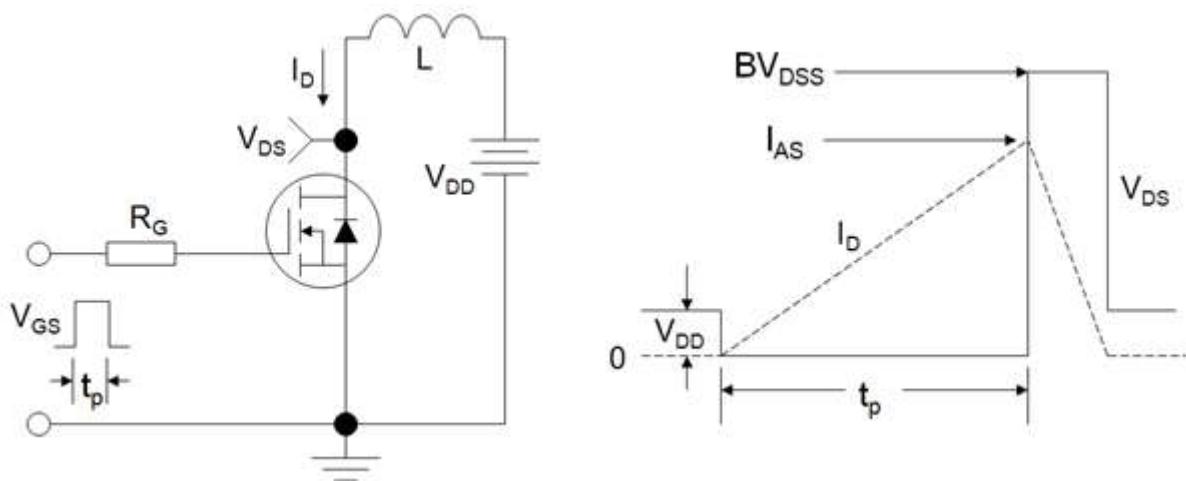


Figure 3 : Unclamped Inductive Switching Test Circuit and Waveform



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## N-Channel Power MOSFET

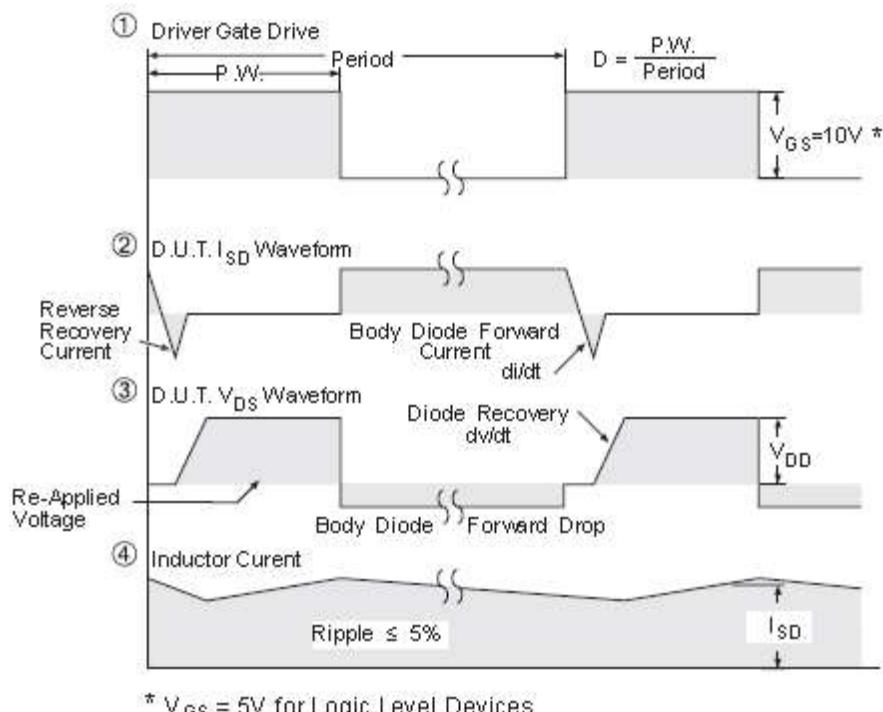
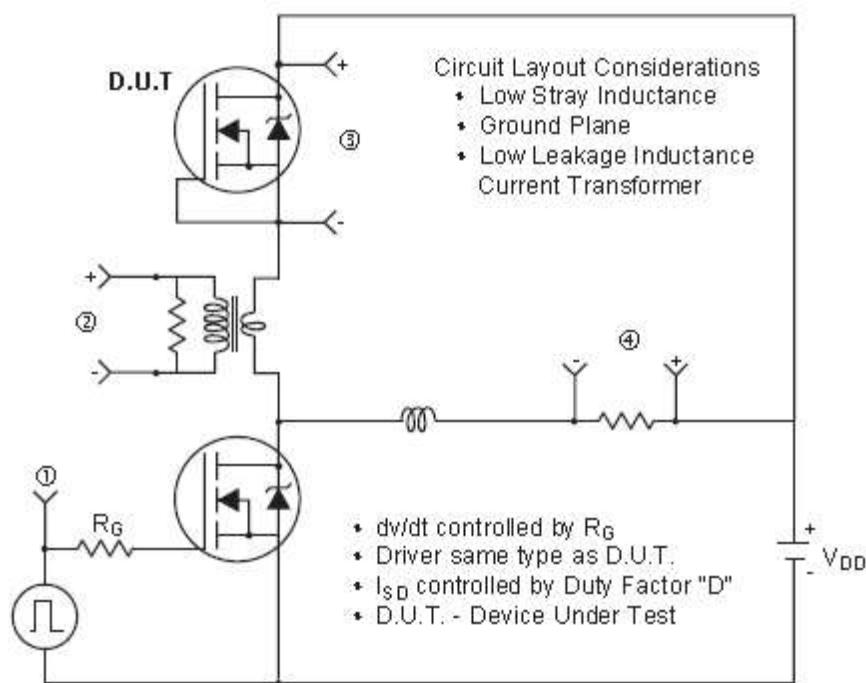
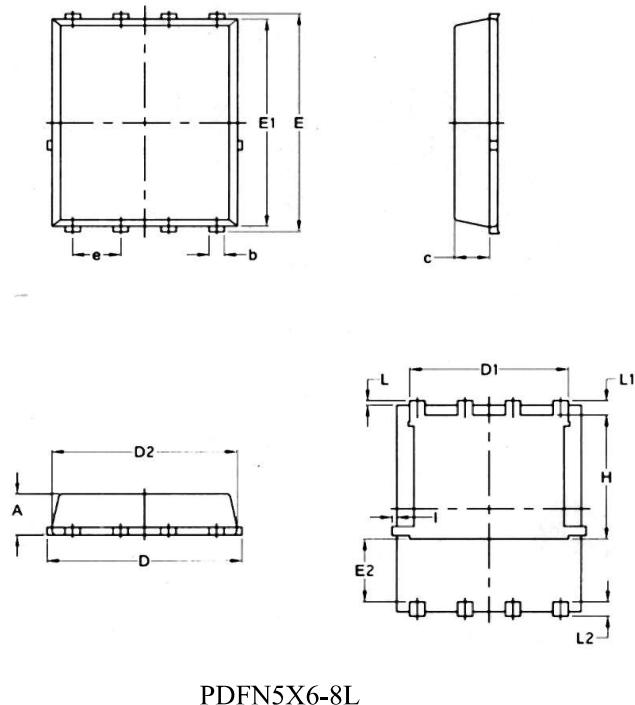


Figure 4:Peak Diode Recovery dv/dt Test Circuit & Waveforms (For N-channel)

# AP150N03G

## N-Channel Power MOSFET

### Package Mechanical Data



PDFN5X6-8L

SYMBOL	COMMON			
	MM		INCH	
	MIN.	MAX.	MIN.	MAX.
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.970	0.0324	0.0382
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	—	0.0630	—
e	1.27	BSC	0.05	BSC
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	—	0.18	—	0.0070