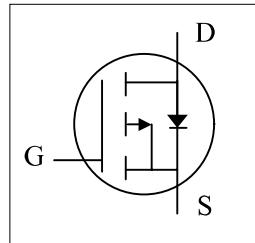


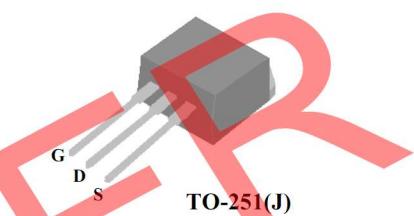
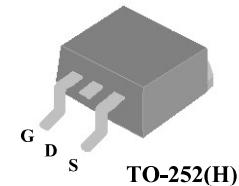
# AP9565K

## P-Channel Power MOSFET

- ▼ Lower Gate Charge
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic
- ▼ RoHS Compliant & Halogen-Free



$BV_{DSS}$	-40V
$R_{DS(ON)}$	42mΩ
$I_D$	-24.5A



## Description

AP9565K series are from Advanced Power innovative design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The TO-252 package is widely preferred for all commercial-industrial surface mount applications using infrared reflow technique and suited for high current application due to the low connection resistance. The through-hole version (AP9565AGJ) are available for low-profile applications.

## Absolute Maximum Ratings@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D@T_c=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$	-24.5	A
$I_D@T_c=100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$	-15.8	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	-72	A
$P_D@T_c=25^\circ\text{C}$	Total Power Dissipation	25	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Value	Units
$R_{thj-c}$	Maximum Thermal Resistance, Junction-case	5.0	°C/W
$R_{thj-a}$	Maximum Thermal Resistance, Junction-ambient (PCB mount) <sup>3</sup>	62.5	°C/W
$R_{thj-a}$	Maximum Thermal Resistance, Junction-ambient	110	°C/W

# AP9565K

## P-Channel Power MOSFET

### Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-40	-	-	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-12\text{A}$	-	-	42	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-9\text{A}$	-	-	60	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1	-	-3	V
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=-10\text{V}, I_{\text{D}}=-16\text{A}$	-	20	-	S
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=-32\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-10	$\mu\text{A}$
	Drain-Source Leakage Current ( $T_j=125^\circ\text{C}$ )	$V_{\text{DS}}=-32\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-250	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage	$V_{\text{GS}}=+20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	+100	nA
$Q_g$	Total Gate Charge	$I_{\text{D}}=-16\text{A}$	-	12	18	nC
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{DS}}=-32\text{V}$	-	2.3	-	nC
$Q_{\text{gd}}$	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=-4.5\text{V}$	-	6.9	-	nC
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DS}}=-20\text{V}$	-	8	-	ns
$t_r$	Rise Time	$I_{\text{D}}=-16\text{A}$	-	34	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time	$R_G=3.3\Omega$	-	26	-	ns
$t_f$	Fall Time	$V_{\text{GS}}=-10\text{V}$	-	74	-	ns
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	850	1360	pF
$C_{\text{oss}}$	Output Capacitance	$V_{\text{DS}}=-25\text{V}$	-	110	-	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance	f=1.0MHz	-	100	-	pF

### Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$V_{\text{SD}}$	Forward On Voltage <sup>2</sup>	$I_{\text{S}}=-16\text{A}, V_{\text{GS}}=0\text{V}$	-	-	-1.2	V
$t_{\text{rr}}$	Reverse Recovery Time	$I_{\text{S}}=-16\text{A}, V_{\text{GS}}=0\text{V},$ $dI/dt=-100\text{A}/\mu\text{s}$	-	26	-	ns
$Q_{\text{rr}}$	Reverse Recovery Charge	$dI/dt=-100\text{A}/\mu\text{s}$	-	20	-	nC

### Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board

# AP9565K

## P-Channel Power MOSFET

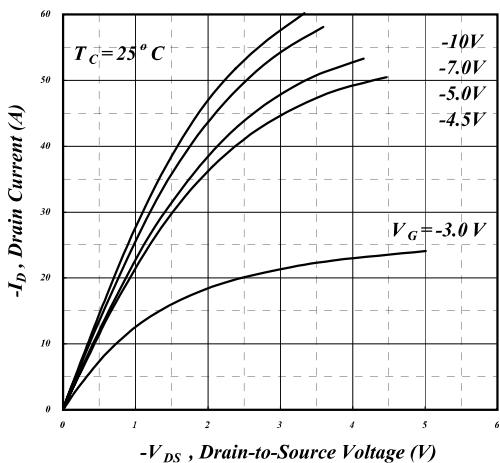


Fig 1. Typical Output Characteristics

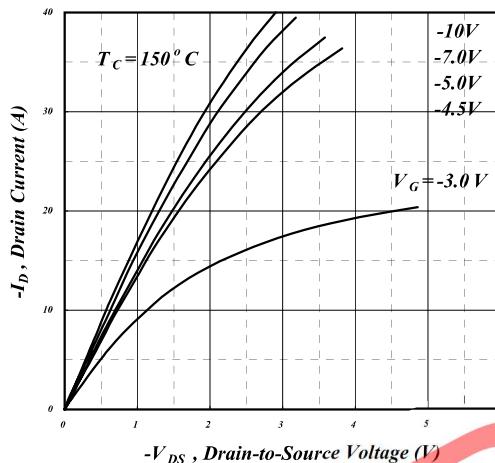


Fig 2. Typical Output Characteristics

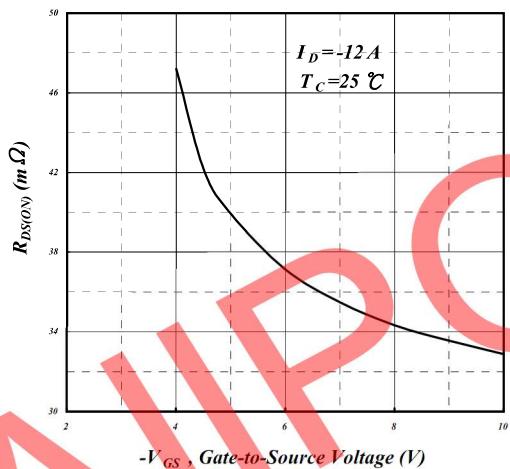


Fig 3. On-Resistance v.s. Gate Voltage

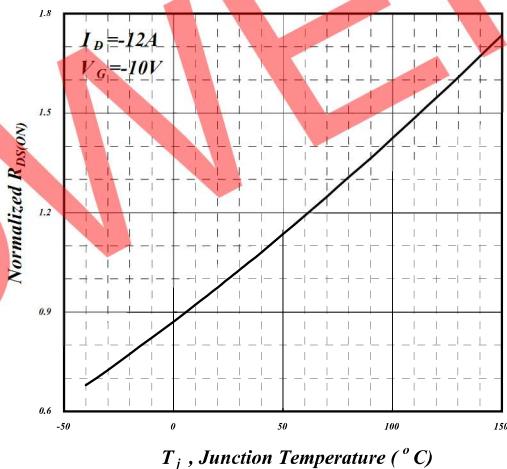


Fig 4. Normalized On-Resistance v.s. Junction Temperature

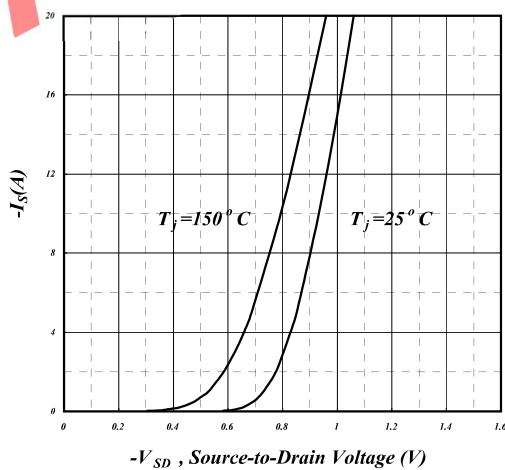


Fig 5. Forward Characteristic of Reverse Diode

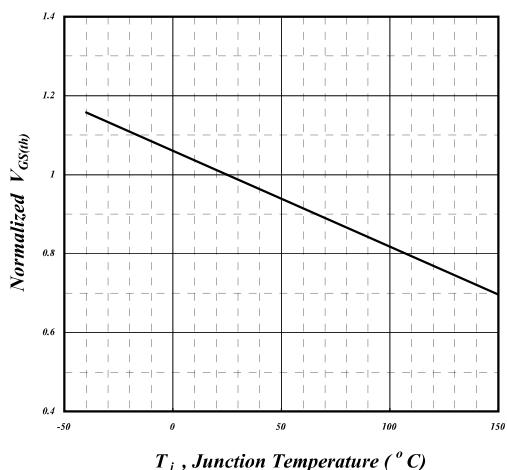


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

# AP9565K

## P-Channel Power MOSFET

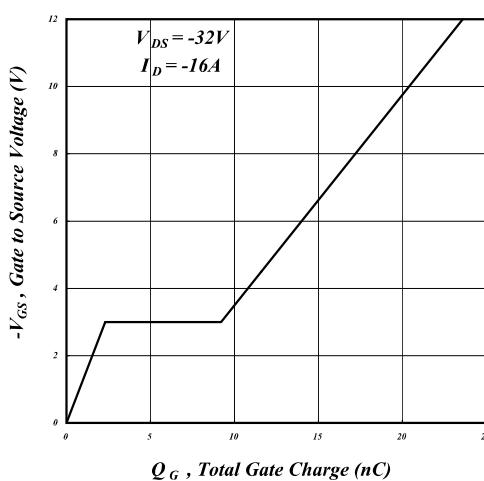


Fig 7. Gate Charge Characteristics

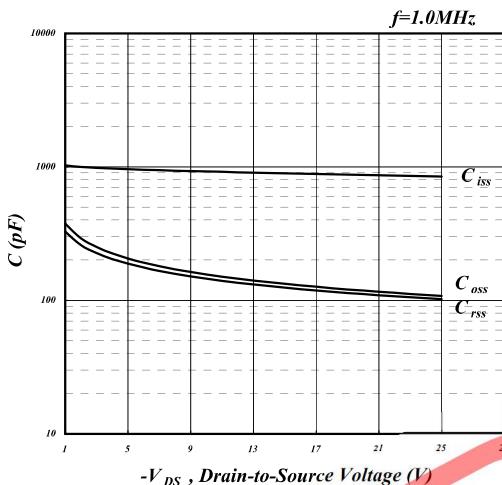


Fig 8. Typical Capacitance Characteristics

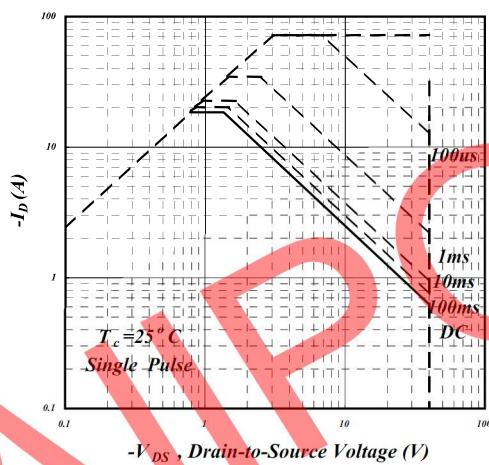


Fig 9. Maximum Safe Operating Area

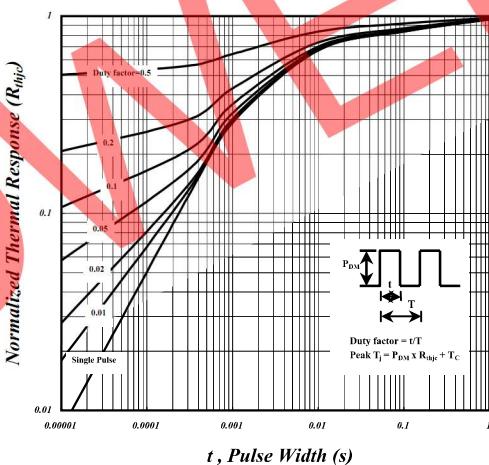


Fig 10. Effective Transient Thermal Impedance

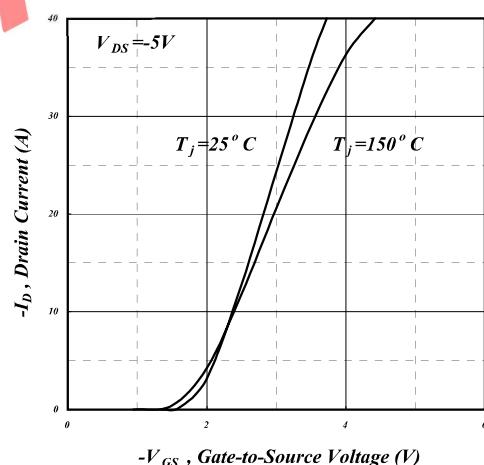


Fig 11. Transfer Characteristics

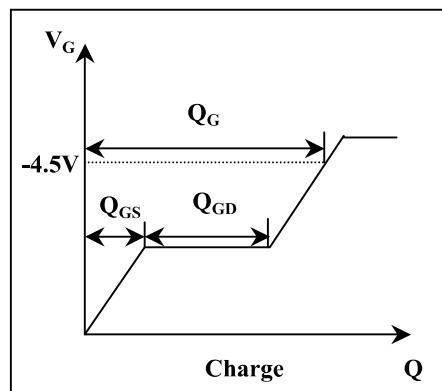


Fig 12. Gate Charge Waveform